

Notice of References Cited	Application/Control No. 10/630,411	Applicant(s)/Patent Under Reexamination MULE ET AL.	
	Examiner Ryan Lepisto	Art Unit 2883	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,351,576	02-2002	Ding, Yi	385/14
	B	US-2003/0034538	02-2003	Brophy et al.	257/444
	C	US-6,690,851	02-2004	Guilfoyle, Peter S.	385/18
	D	US-6,829,286	12-2004	Guilfoyle et al.	372/108
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	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	"Input Coupling and Guided-wave Distribution Schemes for Board-level Intra-chip Guided-Wave Optical Clock Distribution Network Using Volume Grating Coupler Technology", Mule et al, Proceedings of the IEEE, June 2001, p. 128-130.
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	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.